

Weekly Report

Week 2: 23/02/2015 - 27/02/2015

Work Developed

In this week, work was focused on setting up the development environment for future work. This included installing the Vivado Design Suite from Xilinx, reading the get started and hardware debug modules for the ZC706 SoC board from Xilinx, and a review of the Verilog hardware description language.

Challenges

Next Tasks

Next week, work will be focused on starting the development of the system level architecture.