

Weekly Report

Week 11: 27/04/2015 - 01/05/2015

Work Developed

This week, testing of the system was started. At the time of writing of this report, the system is still not functional, as the reading of the status flag always returns the value zero. The problem is probably contained in the AXI Master interfaces writing data to RAM, as there is a signal write done, which indicates that data was successfully written to RAM, that needs to go high for the status flag to be updated.

Challenges

The main challenge when testing the system is the difficulty in accessing the information of the BM3D system, which can only be done by modifying the AXI Slave interface to include more registers that can store the values of important control wires of the system.

Next Tasks

Next week, work will be focused on continuing the testing of the system on the ZYNQ System on Chip.