

DCIS 2001

XVI Conference on

Design of Circuits and Integrated Systems

Porto, Portugal

November 20-23, 2001

Topics of interest include:

Analog and Digital IC Design
CAD Methods and Tools
Logic and High-level Synthesis
Field-Programmable Systems
III-V Integrated Circuits
Analog-Digital Interfacing
Industrial Applications
Signal Processing Applications
Integrated Sensors
Communications Systems
RF Circuits
Low Power Design
Reconfigurable Systems
Modeling and Simulation
Neural and Fuzzy Systems
Special Architectures
Testability and Test Techniques
ICs for Control in Power Applications

The 16th edition of the Conference on Design of Circuits and Integrated Systems (DCIS 2001) will be held in Porto, Portugal, November 20-23, 2001. It is organized by the Department of Electrical and Computer Engineering of FEUP (*Faculdade de Engenharia da Universidade do Porto*) and by INESC Porto (*Instituto de Engenharia de Sistemas e Computadores do Porto*). The purpose of the Conference is to provide a forum to exchange ideas and to show industrial and research results in microelectronics.

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Organization:

Important dates

Deadline for paper submission

April 6, 2001

Notification of acceptance

June 22, 2001

Deadline for final paper submission

July 20, 2001



FEUP
Universidade do Porto
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